

TITLE: PARTIAL PAGE PROGRAMMING OF MULTI LEVEL FLASH  
INVENTOR(S): Allan Parker; Glen Lam  
Attorney Docket #: AMD-G1255

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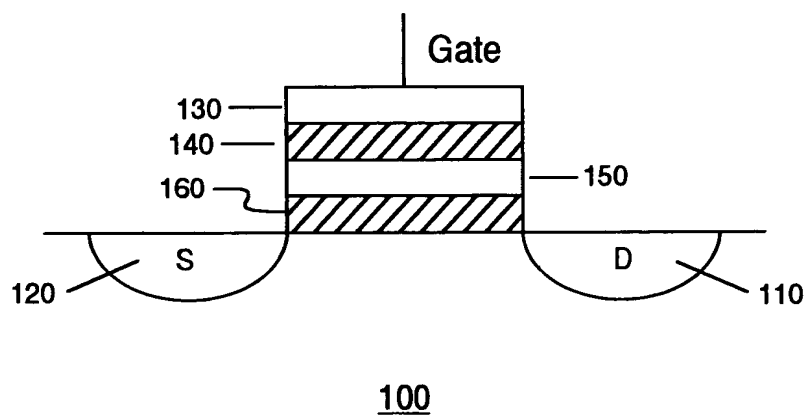


Figure 1



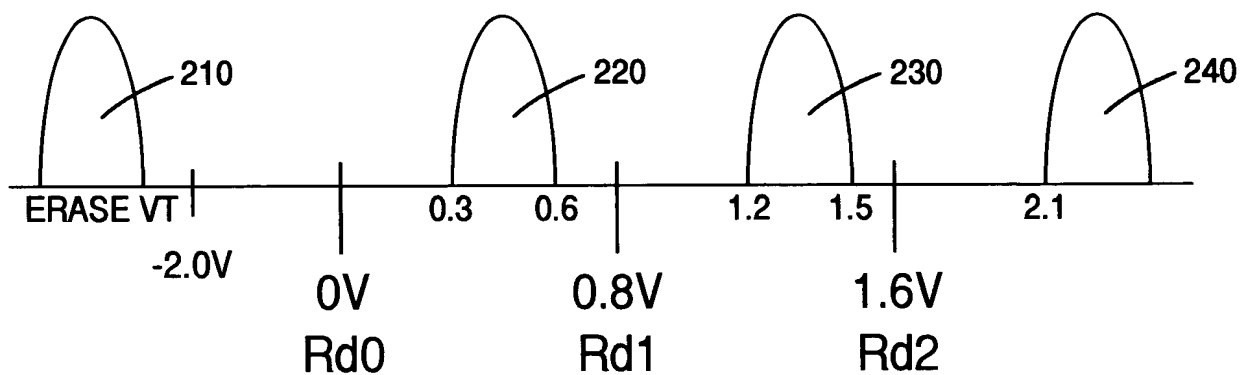
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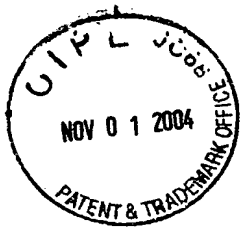
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ASSUME VT DISTRIBUTION  $\leq 300\text{MV}$



200

Figure 2



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LEVEL	Q2	Q1
2	0	0
1	0	1
0	1	0
Erase	1	1

300

Figure 3

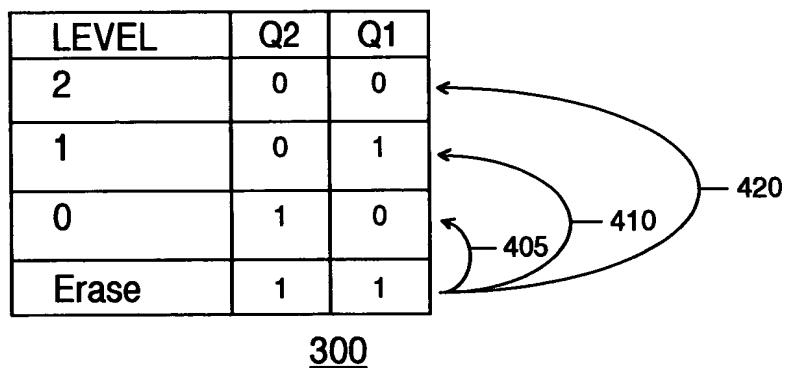
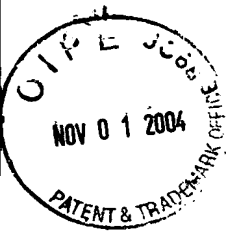



Figure 4A



LEVEL	Q2	Q1
2	0	0
1	0	1
0	1	0
Erase	1	1



300

Figure 4B



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LEVEL	Q2	Q1
2	0	0
1	0	1
0	1	0
Erase	1	1

← 440

300

Figure 4C



500

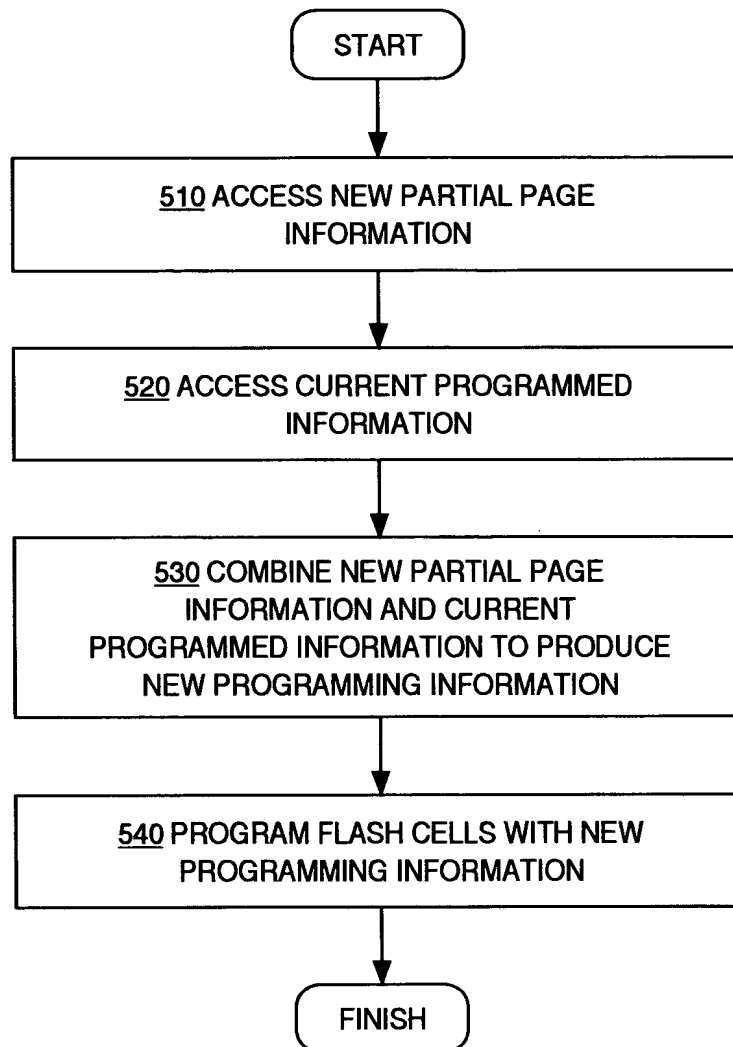


Figure 5



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600

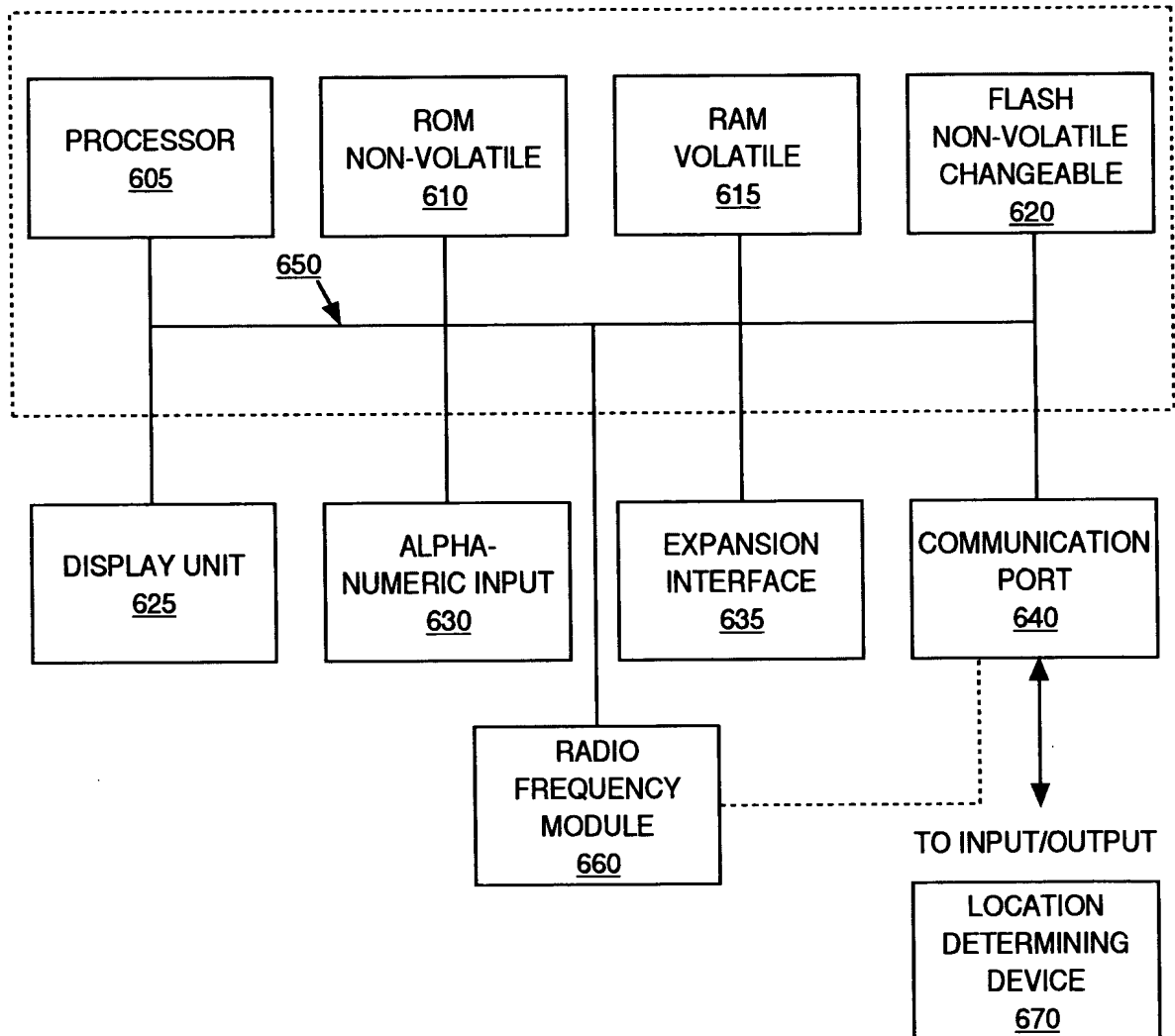


Figure 6